

S.No. 1	Draw the Hardware Design Diagram & Algorithmic flowchart for Multiplying two signed 2's complement binary numbers with Booth technique.
2.	Multiply following two signed magnitude fixed point numbers & show the value of A, Q Registers, E -bit and Sequence Counter in the process. Use 6 bit registers including the sign bit. Show the complete table. (+29) X (-17)
3	Give the procedure for addition & subtraction of floating point numbers. How we convert a given number into a normalized number & prepare it for addition & subtraction. Design the algorithm for Addition & subtraction of floating point numbers.