

1	Which one of the following is not a system software A) Compiler B) Operating System C) Assembler D) Editor E) Loader					
2	Which one of the following is not a addressable unit of the memory A) Byte B) Word C) quadword D) Halfword E) nibble					
3	Which one of the following is not a structured language A) C B) C++ C) Fortran D) Pascal E) Java					
4	When we allow multiple programs to reside in separate areas of core memory at the same time, it is called A) Multiprocessing B) Multiprogramming C) Time sharing D) Paging E) Multithreading					
5	A copy of the current instruction is stored in a) MAR B) IR C) LC D) General registers E) status word					
6	In the instruction A 1,4(5,6) The Base register is A) 6 B) 5 C) 4 D) 1 E) not specified					
7	If 000000010000001000000001100000100 is a unpacked decimal representation then the no. is A) 1020304 B) -1234 C) 1048576 D) -1048576 E) 1234					
8	$(AABC)_{16} = (\quad)_8$ A)123456 B)127890 C)125274 D)324512 E) 128765					
9	$(96.28)_{10} = (\quad)_{16}$ A)6.47AE14 B)A.BCD C)6.4 D)6.8765 E) 10.6534					
10	<table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td>OP</td> <td>R1</td> <td>R3</td> <td>B2</td> <td>D2</td> </tr> </table> This is instruction format of type A) RR B) RX C) RS D) SI E) SS	OP	R1	R3	B2	D2
OP	R1	R3	B2	D2		
11	01011010001100000101000000010000 is an RX instruction , The Index Register in this instruction is A) Zero B) One C) Two D) Three E) Five					
12	For Boolean AND the instruction is A) X B) AND c) BAND D) N E) AN					
13	If register 1 contains 60 then L 2,904(0,1) instruction A) Loads 1 into register 2					

	<p>B) Loads 60 into register 2</p> <p>C) Loads 0 into register 2</p> <p>D) Loads C(964) in register 2</p> <p>E) Loads C(904) in register 2</p>
14	<p>One of the following is not a pseudo operation</p> <p>A) EQU B) USING C) START D) BALR E) END</p>
15	<p>One of the following is a pseudo operation</p> <p>A) STMC B) LTOrg C) ZAP D) SRDA E)PACK</p>
16	<p>If register 2 contains 10101010101010101010101010101010</p> <p>Then the result of SRDA 2,5 will be</p> <p>a) 00000101010101010101010101010101</p> <p>b) 11111101010101010101010101010101</p> <p>c) 01010101010101010101010101000000</p> <p>d) 01010101010101010101010101011111</p> <p>e) 01010101010101010101010101010101</p>
17	<p>Which of the following tables is not created in pass one.</p> <p>A) MOT B) POT C) BT D) LT E) ST</p>
18	<p>The length of a record in the symbol table is</p> <p>A) 8 bytes B) 4 bytes C) 14 bytes D) 6 bytes E) 10 bytes</p>
19	<p>Program status word in IBM 360 is of length</p> <p>A) 64 bits B) 32 bits C) 16 bits D) 128 bits E) 40 bits</p>
20	<p>BR R2 instruction is converted into</p> <p>A) BC 2,R2 B) BCR 0,R2 C) BCR 15,R2 D) BC 7,R2 E) BC 13,R2</p>
21	<p>In IBM 360 assembler V symbol indicates</p> <p>A) hexadecimal digit B) extended floating point C) value of address</p> <p>D) externally defined address value E) character</p>
22	<p>In IBM 360 assembler H symbol indicates</p> <p>A) binary digit B) word C) packed decimal D) zoned decimal</p> <p>E) halfword</p>
23	<p>SR is an instruction of format</p> <p>A) RR B) SS C) SI D) RX E) RS</p>

24	In IA-32 architecture there are total ____ Segment registers. A) 4 B) 6 C) 8 D) 2 E) 10
25	The control word in IA-32 architecture is of A) 8 bits B) 16 bits C) 24 bits D) 32 bits E) 64 bits
26	* in assembly program means A) Blank B) Wild character C) current address D) previous address E) Comment
27	Which of the following is a absolute instruction A) L DATABASE, = A(DATA1) B) A AC,=F'5' C) C INDEX, = F'8000' D) SAVE DS 2000F E) TOTAL EQU 13
28	The assembly language is not preferred by the programmers A) The error checking is not easy B) Very difficult to program C) All the internal tasks related to program has to be specified by the programmer D) Designing an efficient program is difficult E) All of the above
29	One of the following is not an add instruction available in IBM 360 A) A B) AZ C) AP D) AR E) AL
30	LM instruction will A) Load & multiply registers B) multiply & Load registers C) Load Multiple registers D) Load & Mask the value E) Load & unmask the value
31	If the contents of register 15 are 10, & 10 is stored at memory location 210 then the result of the XI instruction XI 200(15),10 will be A) 15 B) 16 C) zero D) 20 E) 210
32	DC H'2',B'0010' the length of the instruction will be A) 5 bytes B) 3 bytes C) 2 bytes D) 6 bytes E)1 byte