

S.No	Question
1	<p>What is not part of the CPU</p> <p>a) Control Unit                      b) Arithmetic and Logical Unit</p> <p>c) Registers                              d) Power Unit</p>
2	<p>The sequence in which various phases follow</p> <p>a) Fetch-decode-execute                      b) Execute-fetch-decode</p> <p>c) decode-fetch-execute                      d) Fetch-execute-decode</p>
3	<p>External Fragmentation is a disadvantage of</p> <p>a) Single contiguous Allocation                      b) Fixed partitioned allocation</p> <p>c) relocatable variable partitioned allocation      d) variable partitioned allocation</p>
4	<p>If a page has to be moved back to the secondary memory or not is related to</p> <p>a) Judgement field                              b) status field</p> <p>c) block field                                      d) page field</p>
5	<p>Elimination of blank space, tabs, comments is done in</p> <p>a) intermediate code generation      b) syntax phase</p> <p>c) syntactic phase                              d) lexical phase</p>
6	<p>Code optimization phase in the compiler is followed by</p> <p>a) Code generation                              b) Syntactic analysis</p> <p>c) Semantic analysis                              d) Lexical Analysis</p>
7	<p>one of the following is available not only in Unix</p> <p>a) starwriter                                      b) pico</p> <p>c) emacs    d) wordperfect</p>
8	<p>The debugger pauses the program for user's inspection is called</p> <p>a) breakpoint                                      b) set point</p> <p>c) main point                                      d) control point</p>
9	<p>Naming convention in the DOS consists of</p> <p>A) Logical Drive/filename/path      b) Pathname/ file name./ Drive</p> <p>c) Filename/ Drive/Pathname      d) Logical Drive/Path/filename</p>
10	<p>The compulsory file system in Unix is known as</p> <p>a) Root      b) Boot      c) Inode      d) mount</p>

11	Externally defined address is denoted in IBM 360 by a) A b) C c) X d) V					
12	The instruction for exclusive OR of SI format is a) XI B) XC c) X d) XR					
13	01011010001100000101000000010000 is an RX instruction , The Index Register in this instruction is A) Zero B) One C) Three D) Two E) Five					
14	LM instruction will A) Load Multiple registers B) multiply & Load registers C) Load & multiply registers D) Load & Mask the value E) Load & unmask the value					
15	One of the following is a pseudo operation A) STMC B) SRDA C) ZAP D) LTORG E)PACK					
16	<table border="1" style="display: inline-table; vertical-align: top;"> <tr> <td>OP</td> <td>R1</td> <td>R3</td> <td>B2</td> <td>D2</td> </tr> </table> This is instruction format of type A) RS B) RX C) RR D) SI E) SS	OP	R1	R3	B2	D2
OP	R1	R3	B2	D2		