

1. What is RS & RX Format? Give an example.
2. Why we use LTOrg in an assembly program.
3. How MAR & MBR registers are used in context of Fetch-decode–execute cycle.
4. Explain SRDL assembly instruction.
5. What you mean by Demand Paging.
6. Explain with diagram Inode structure of UNIX.
7. What is Dead code elimination? Give example.
8. In a debugger what is a Conditional Breakpoint.